Latest Features in Simulink Coder

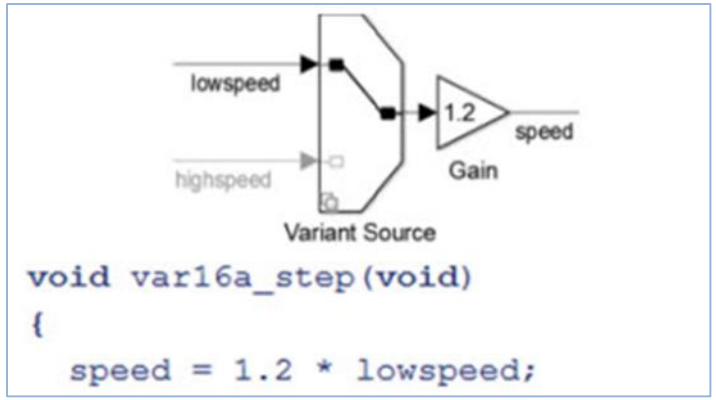
March 2016

R2016a

Design Variants

Generate code for active design variant using Variant Source/ Sink blocks

- Use Variant Source/Sink blocks to design variants instead of placing blocks inside variant subsystems or model variants
- Connections to inactive ports are ignored during simulation

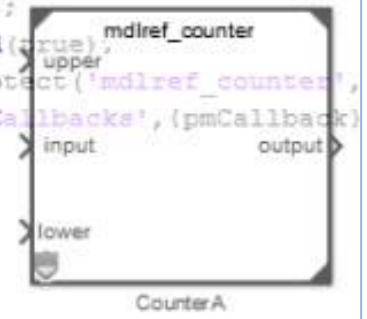


Protected Model Callbacks

Define callbacks for customized protected models

- Specify code to execute when viewing, simulating, or generating code for protected model

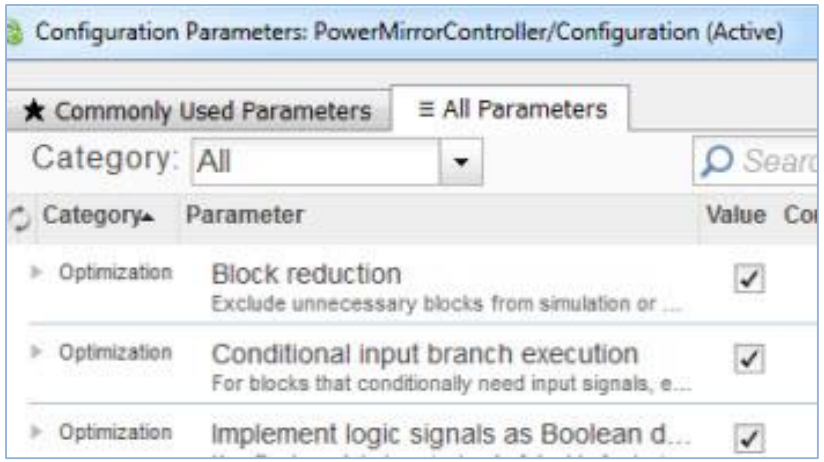
```
pmCallback = Simulink.ProtectedModel.Callback('Bu
'CODEGEN', 'pm_callback.m');
pmCallback.setOverrideBuild(true);
Simulink.ModelReference.protect('mdlref_counter',
'Mode', 'CodeGeneration','Callbacks',{pmCallback}
rtwbuild('mdlref_basic');
```



Simplified Configuration Parameters

Configure models more easily via simplified code generation panes

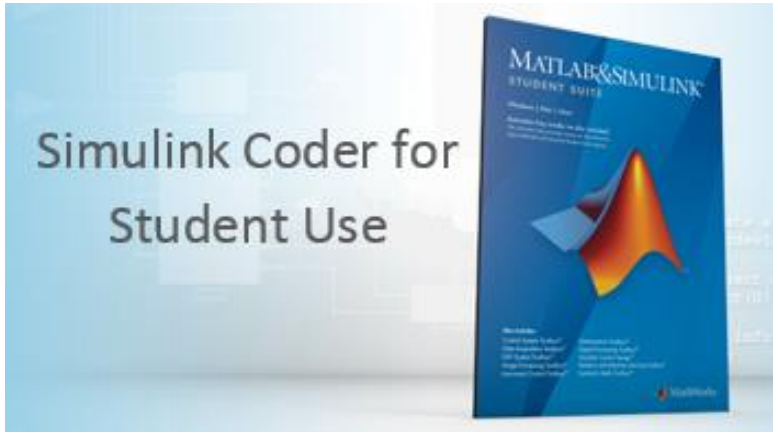
- Category panes display only parameters most useful for configuring models for code generation



Simulink Coder Student Access

Use Simulink Coder as student-use add-on product

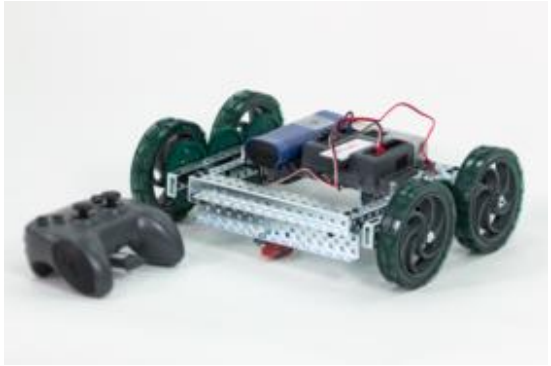
- Get as add-on product with MATLAB Primary and Secondary School Suite
- Student-use software provides the same tools that professional engineers and scientists use

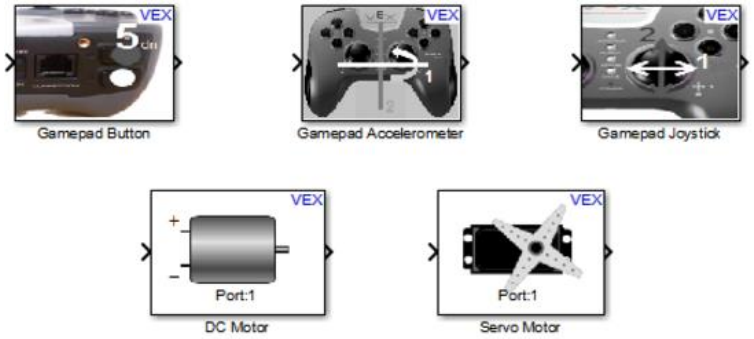


Support for Vex from Simulink Coder

Code generation support for VEX Cortex Microcontroller used in VEX Robotics and BEST Robotics

- Provides blocks for servomotors, DC motors, switches, potentiometers, and VEX gamepad inputs
- Previously required Embedded Coder



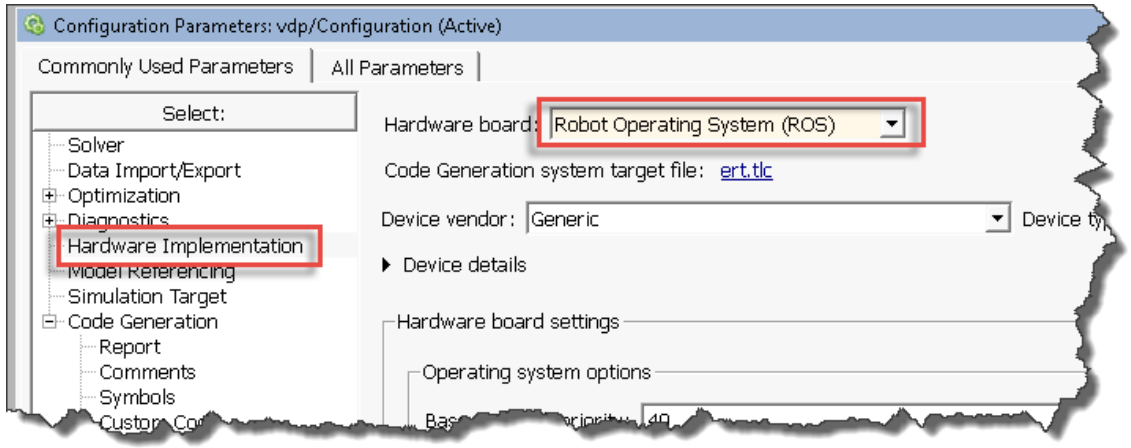


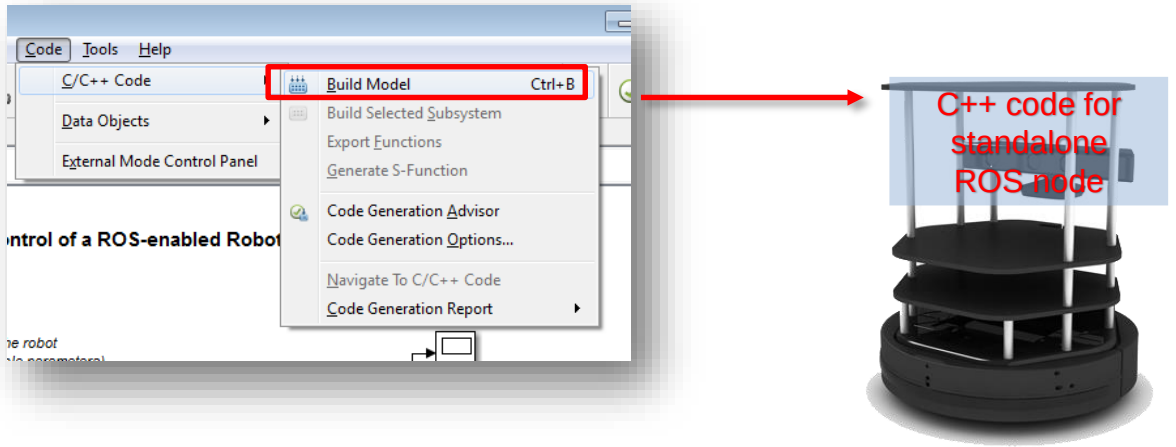
Learn more at www.mathworks.com/hardware-support/vex-arm-cortex.html

Generate Standalone ROS Nodes

Code generation for Robot Operating System

- Generate standalone Robot Operating System (ROS) nodes from Simulink models with just MATLAB Coder and Simulink Coder
- Embedded Coder can optionally be used to customize the generated code






```
» robotROSCodeGenerationExample
```

Model Block Virtual Buses

Use virtual bus to interface with Model blocks

- Exchange signal data through variables or pointers corresponding to bus element, instead of structure
- Improves efficiency by eliminating memory consumption of structure